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FIG. 1
PRIOR ART

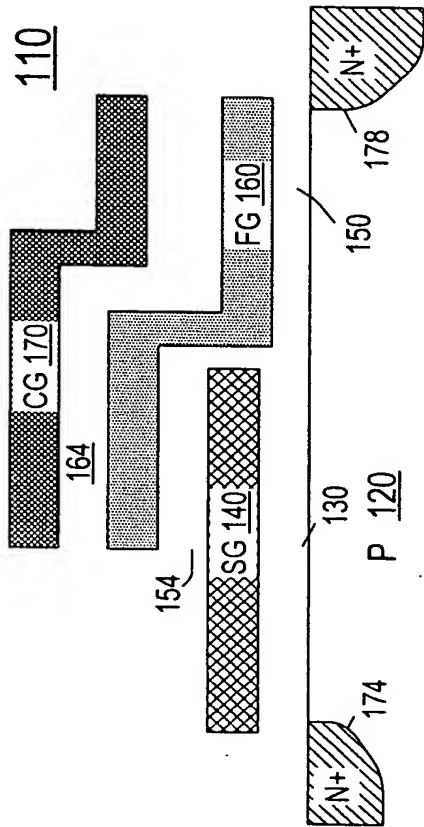
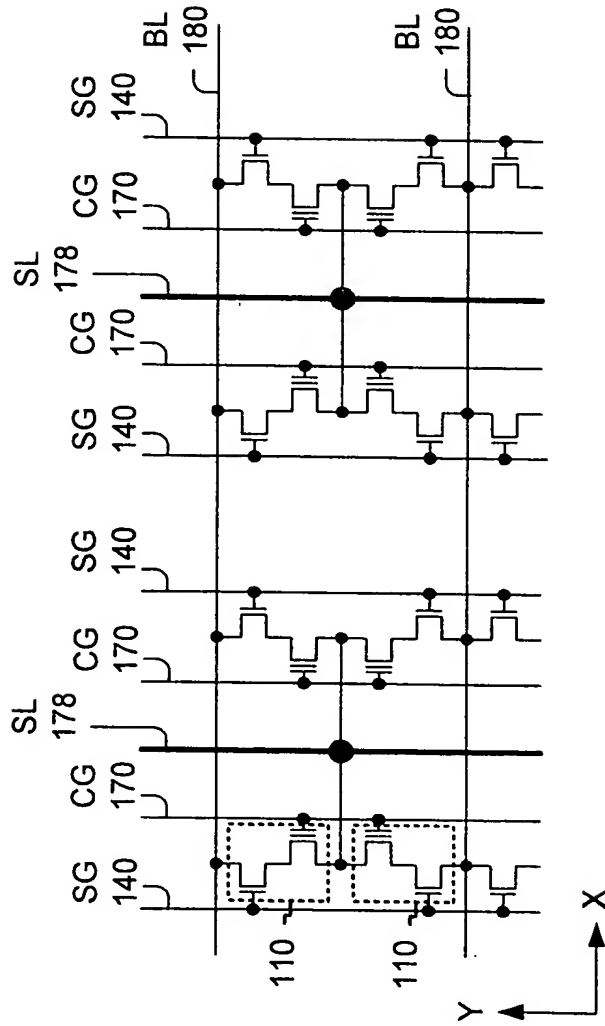


FIG. 2
PRIOR ART



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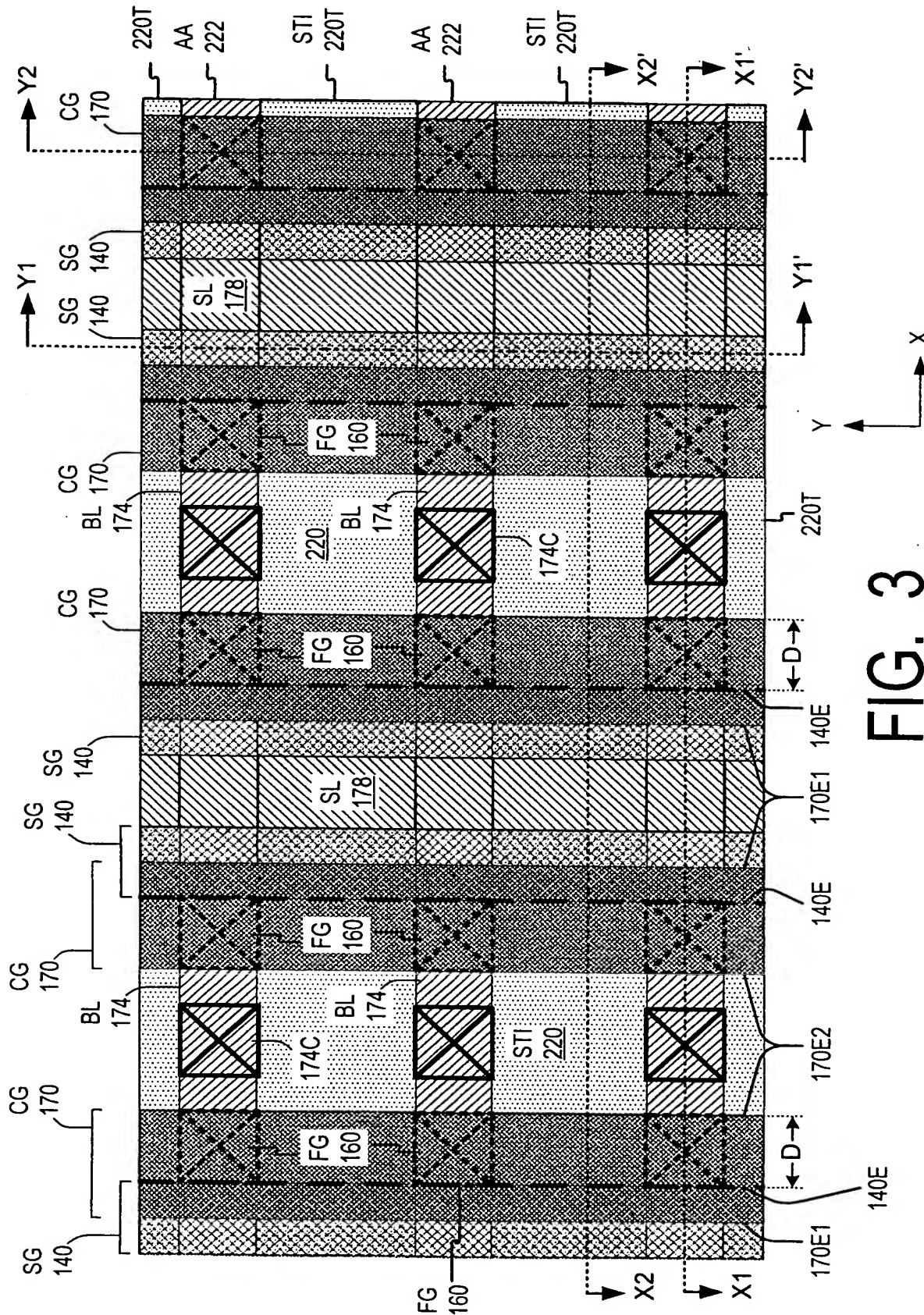


FIG. 3

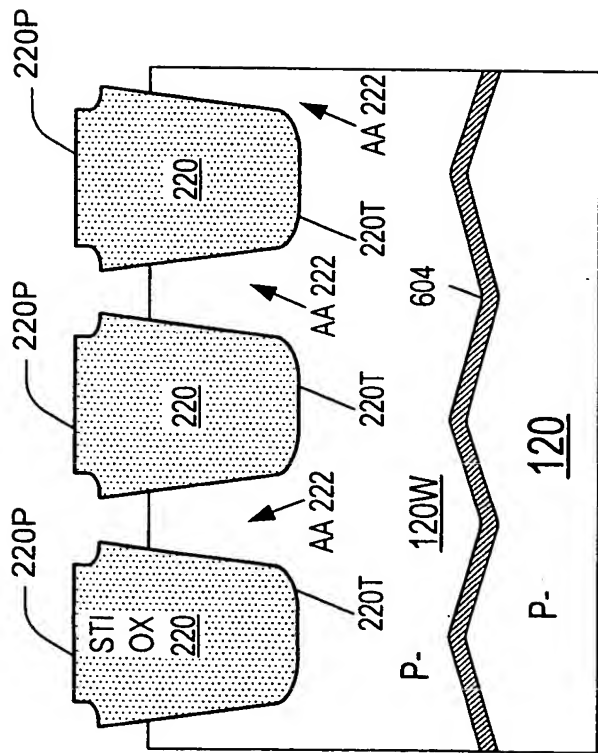


FIG. 4 (Y1-Y1')

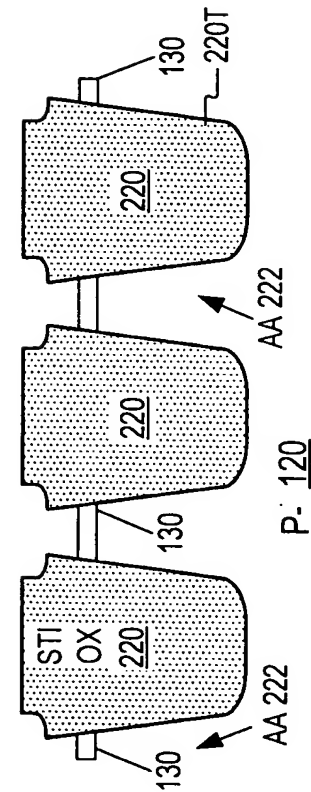


FIG. 5A (Y1-Y1')

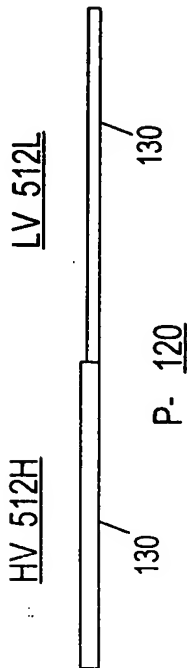


FIG. 5B
(PERIPHERY)

FIG. 6B
(PERIPHERY)

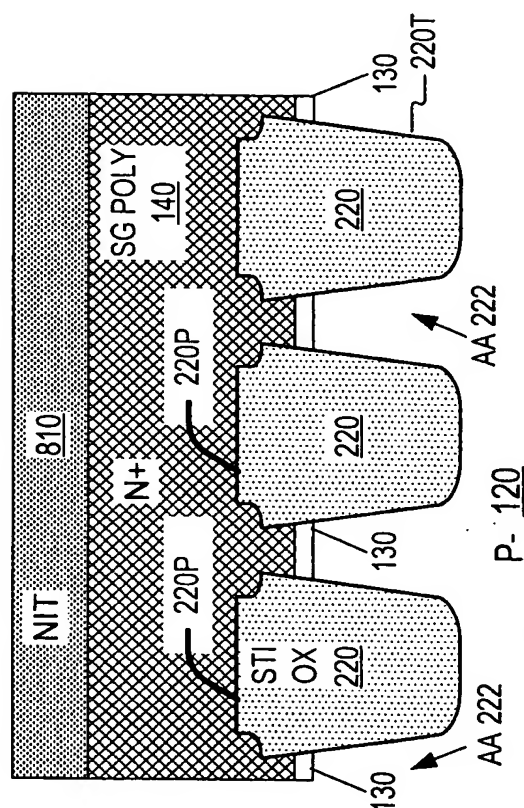
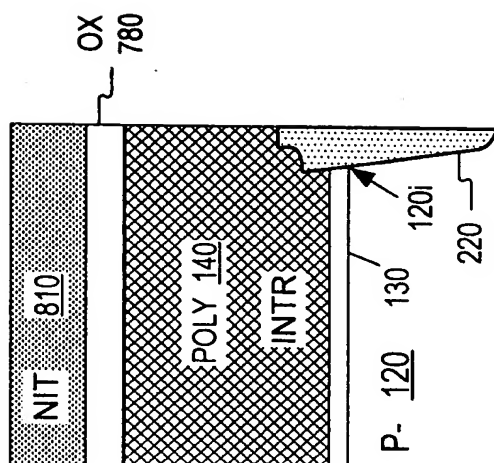


FIG. 6A (Y1-Y1')

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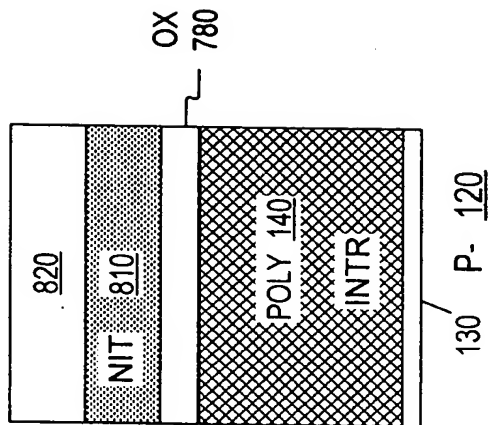


FIG. 7B
(PERIPHERY)

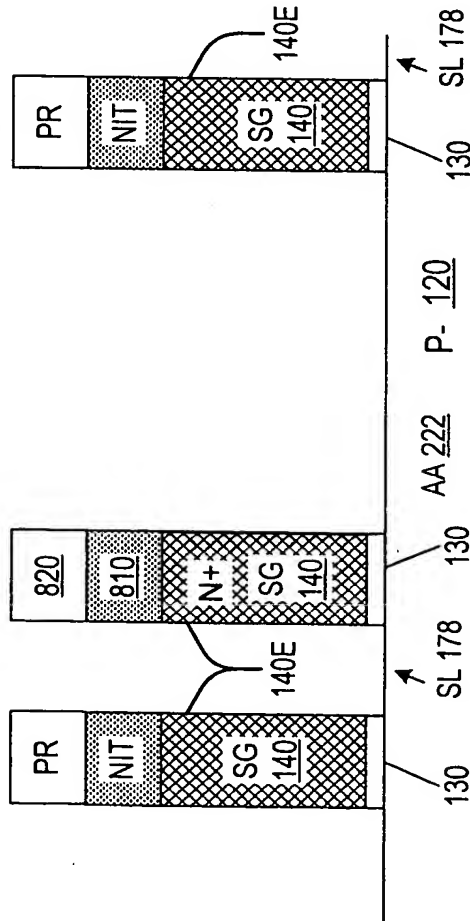


FIG. 7A (X1-X1')

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FIG. 8
(X1-X1')

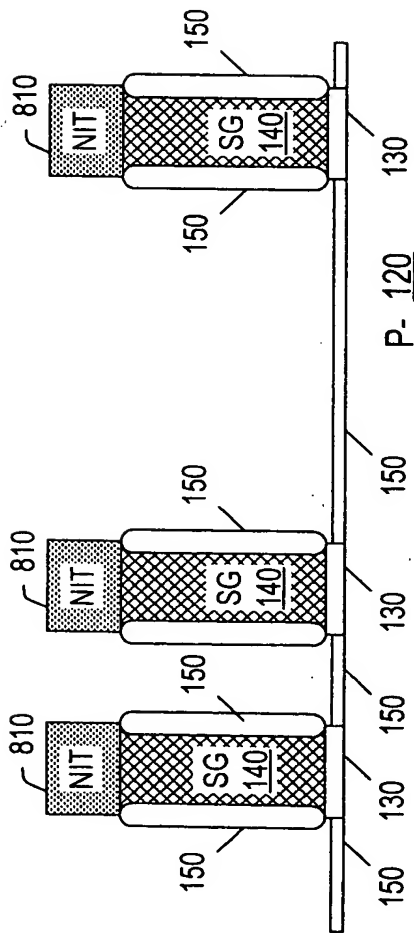
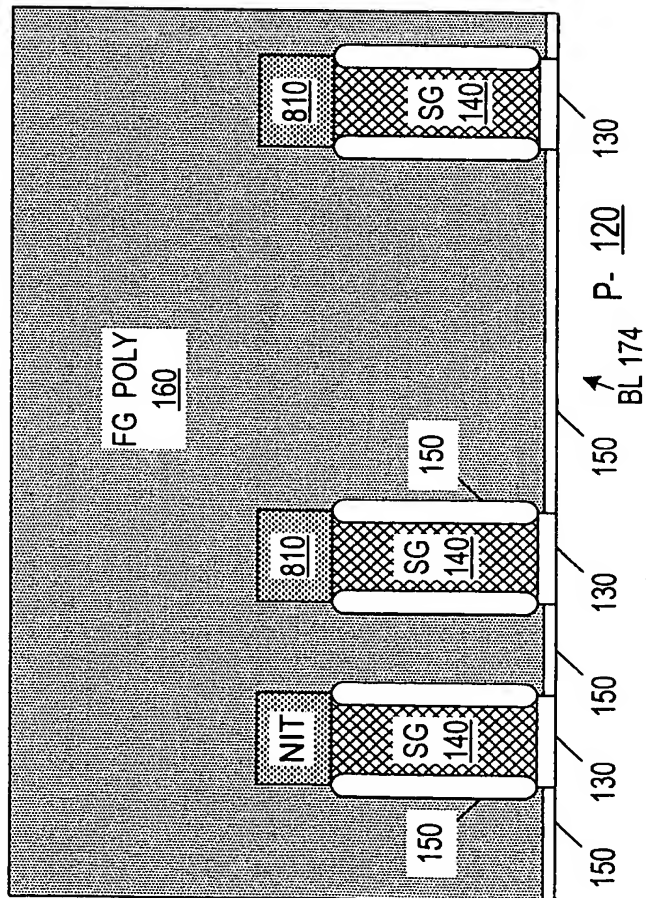


FIG. 9
(X1-X1')



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FIG. 10
(X1-X1')

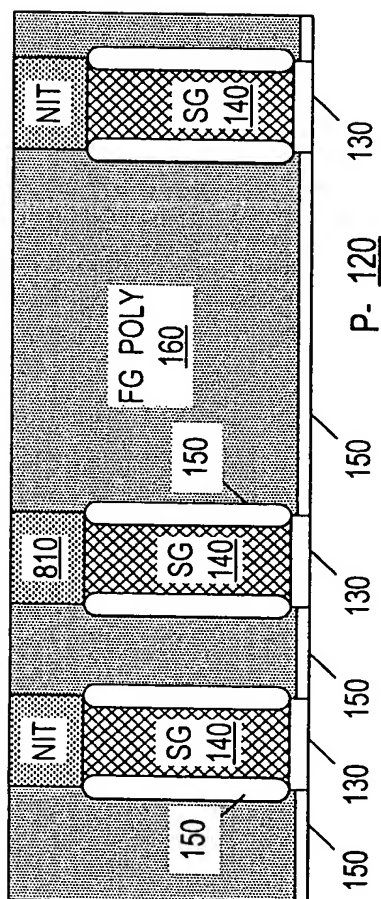


FIG. 11A
(X1-X1')

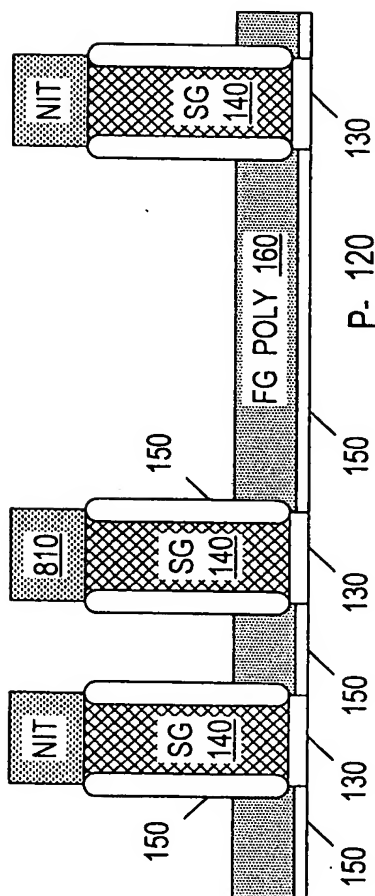
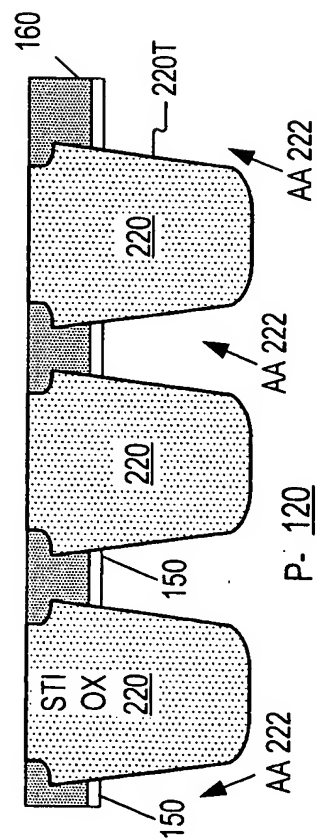


FIG. 11B
(Y2-Y2')



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FIG. 12
(Y2-Y2')

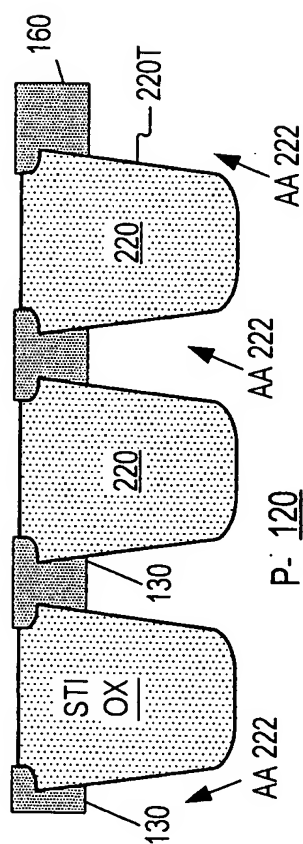
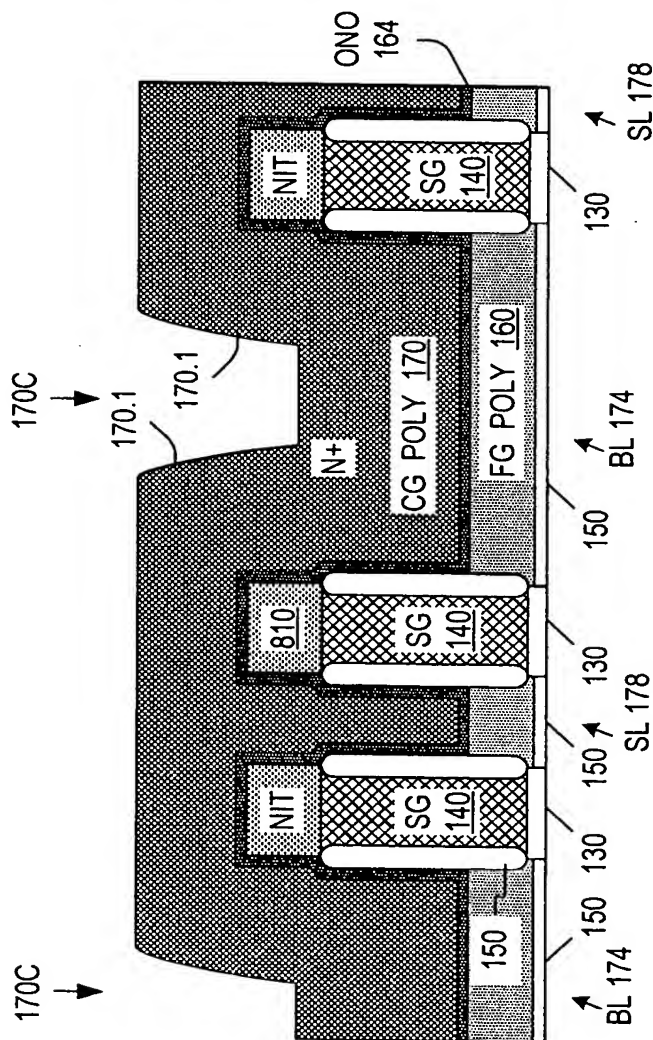


FIG. 13A (X1-X1')



PERIPHERY

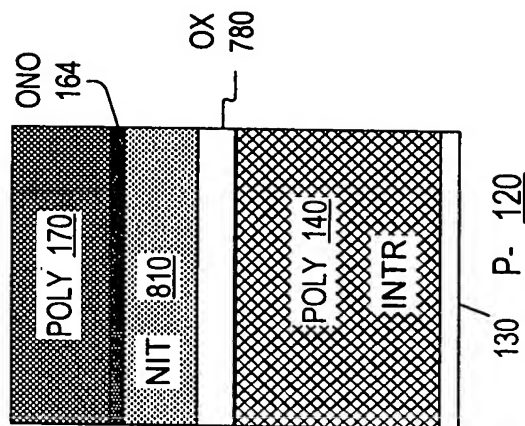


FIG. 13B

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FIG. 14
(X1-X1')

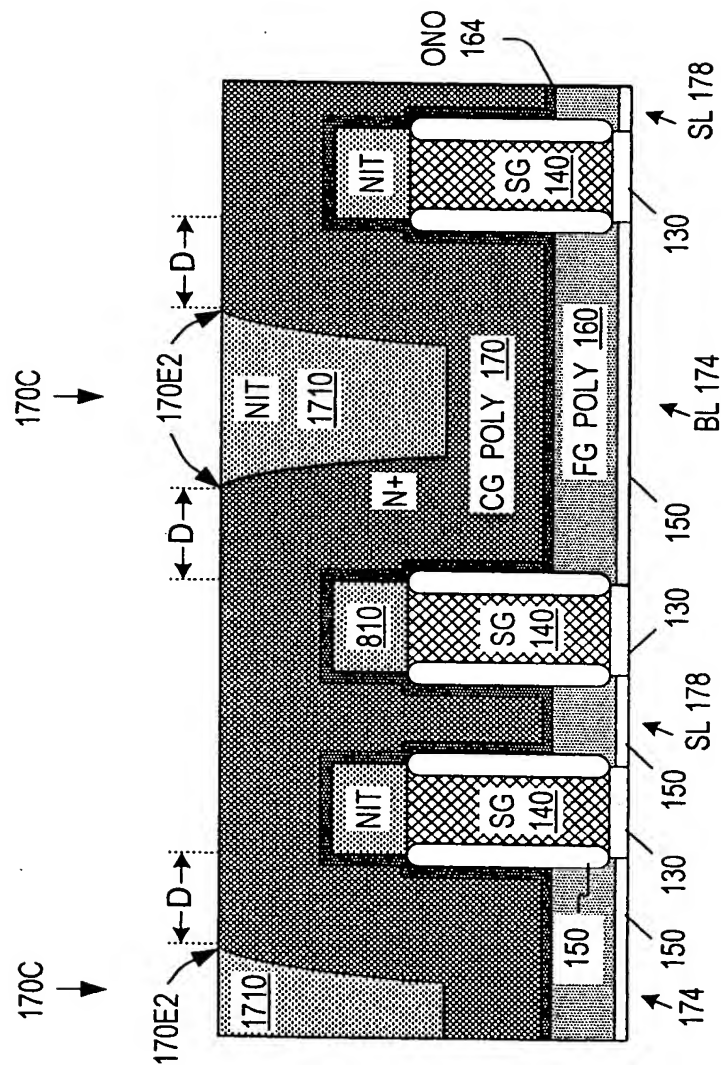
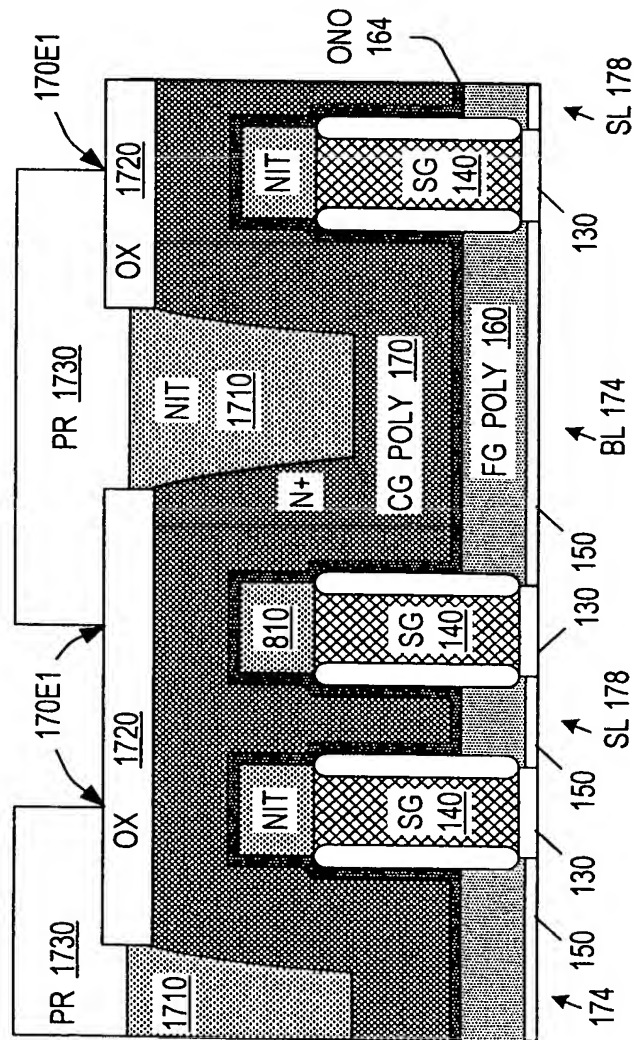


FIG. 16
(X1-X1')



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FIG. 17A
(X1-X1')

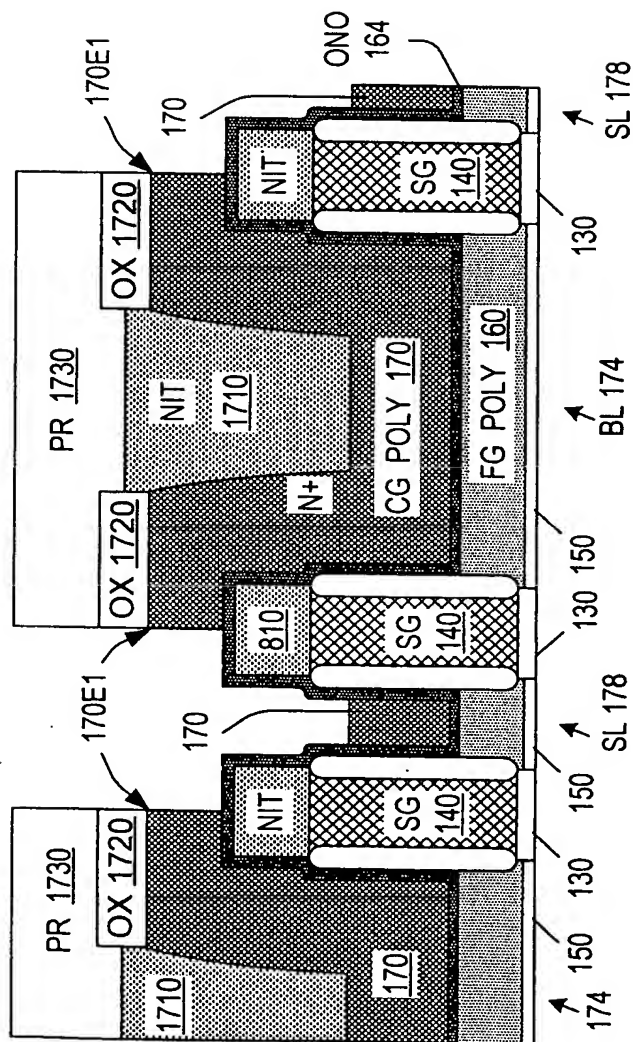
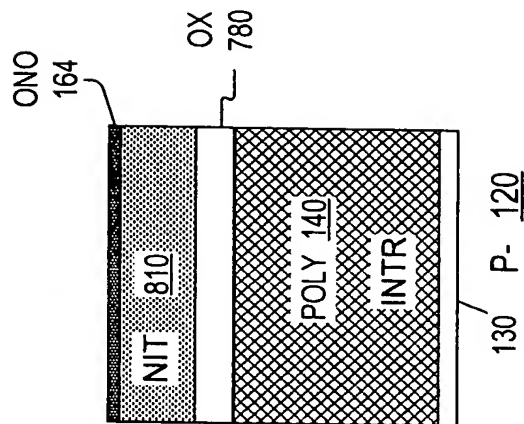


FIG. 17B
(PERIPHERY)



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FIG. 18
(X1-X1')

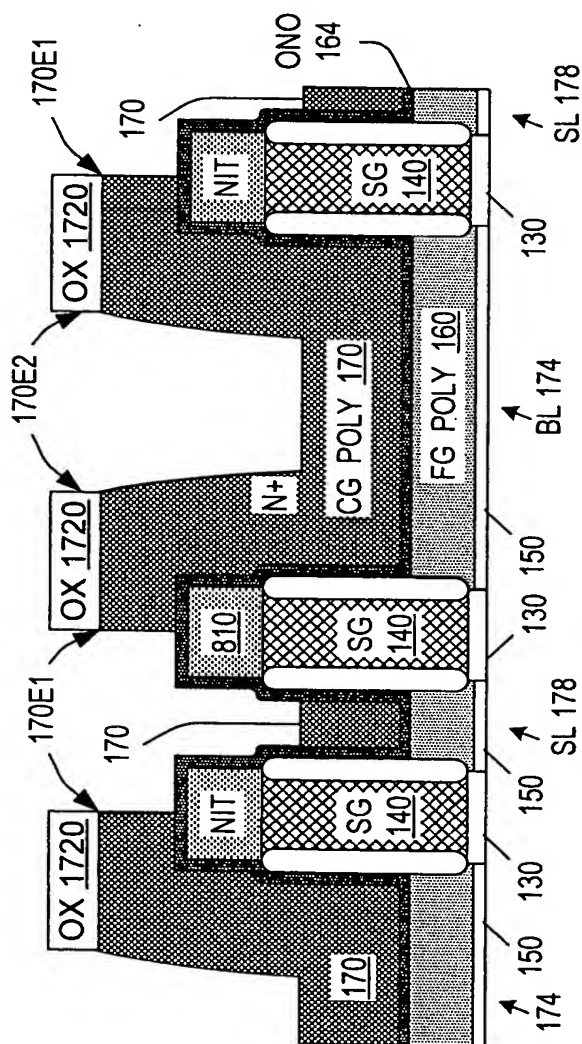


FIG. 19A
(X1-X1')

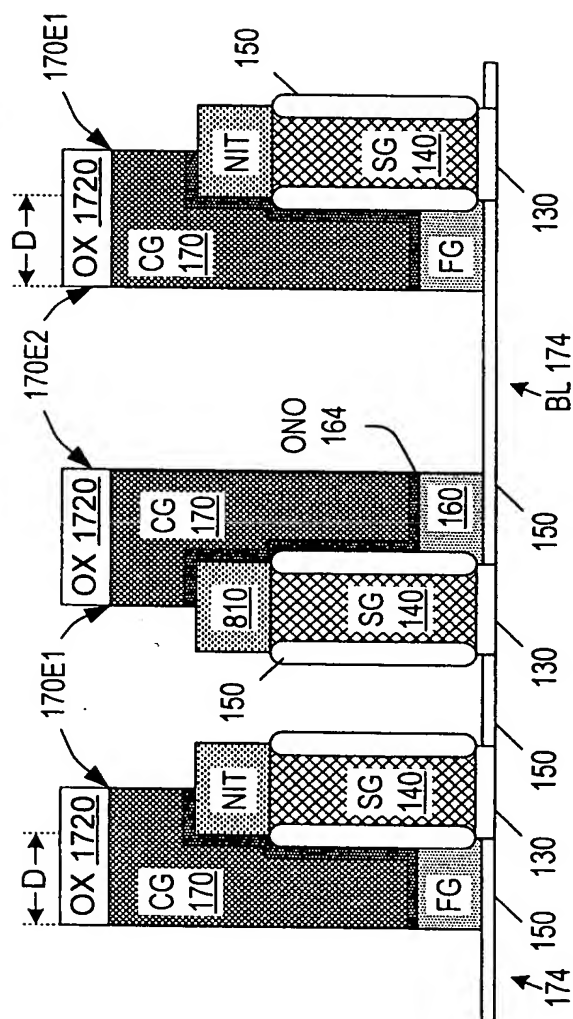
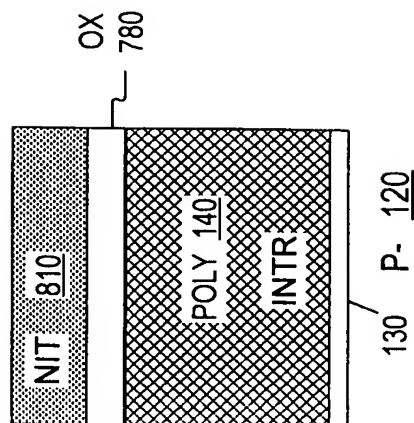
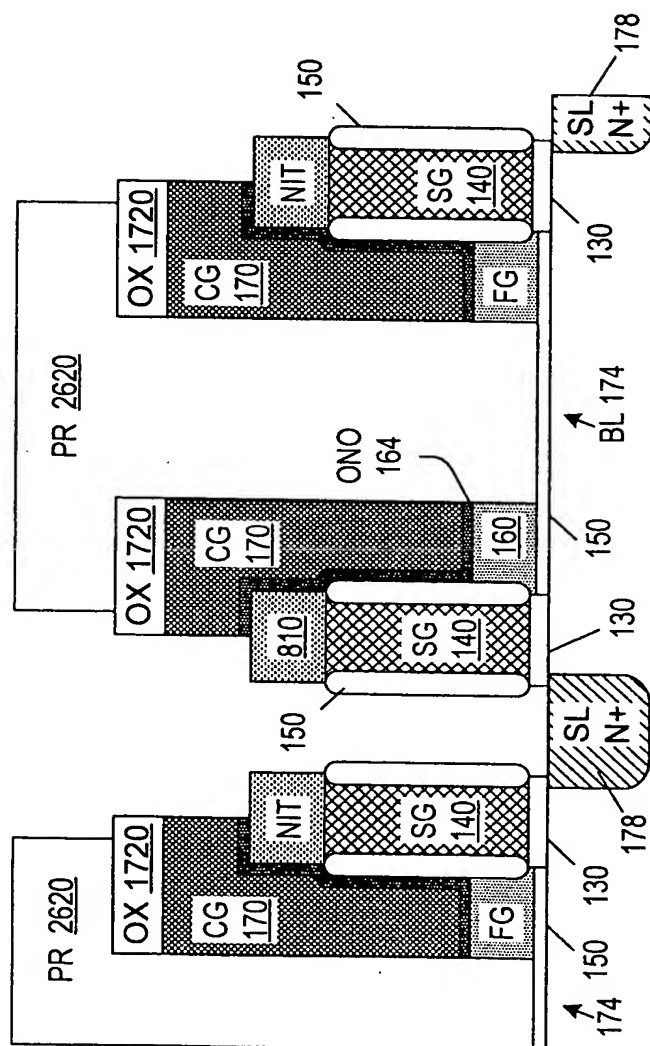


FIG. 19B
(PERIPHERY)



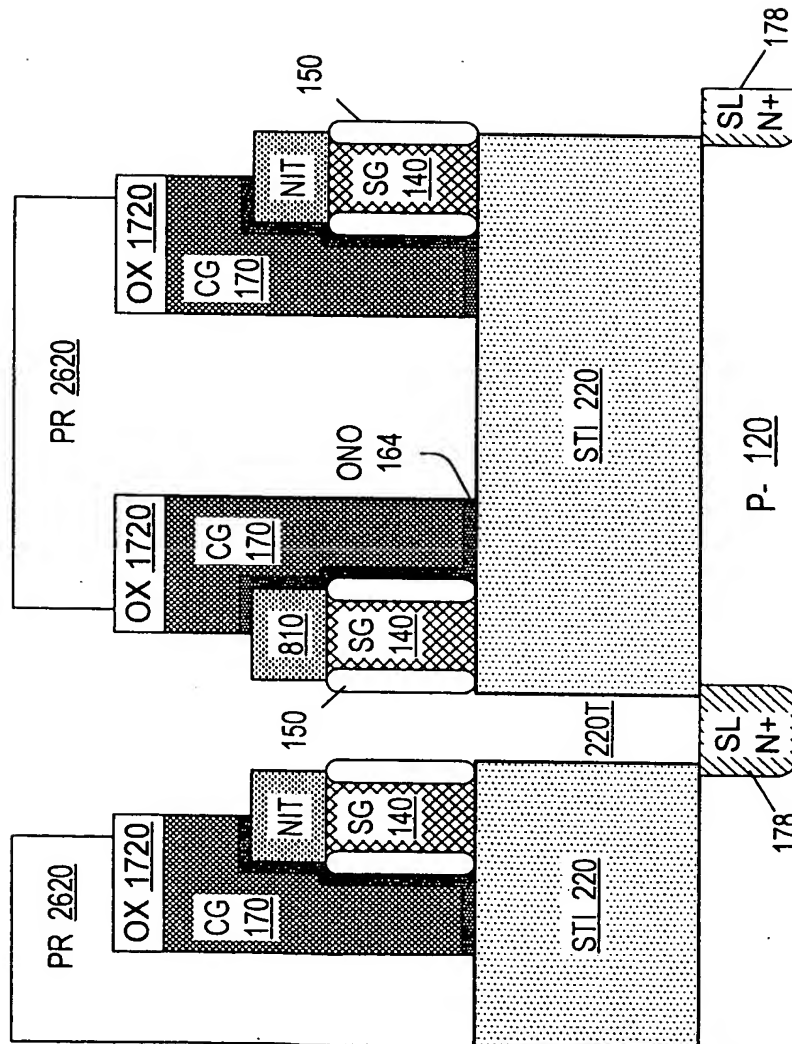
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FIG. 20A
(X1-X1')



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FIG. 20B
(X2-X2')



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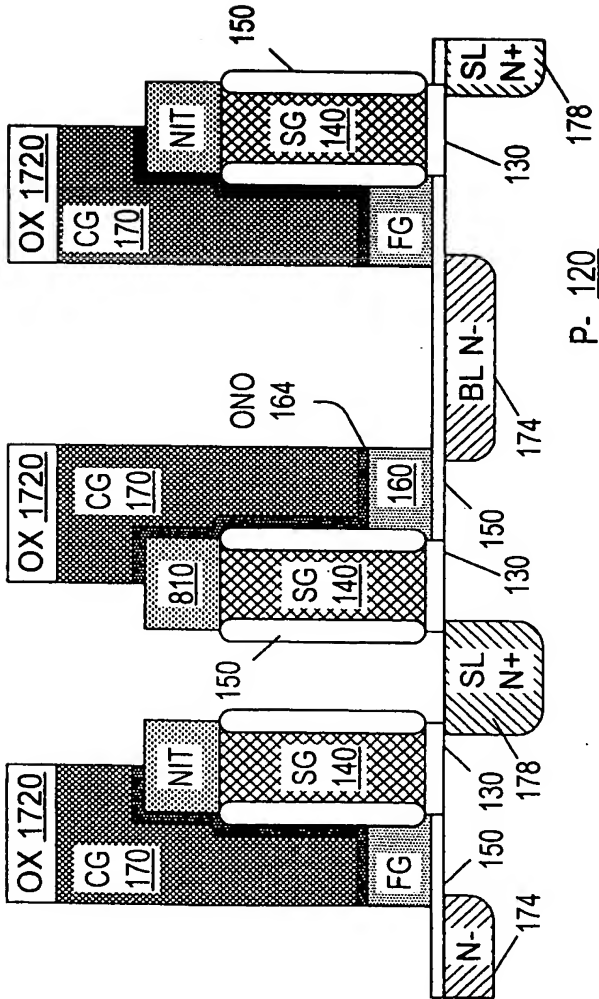


FIG. 21A
(X1-X1')

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PMOS 512P

NMOS 512N

PMOS 512P

NMOS 512N

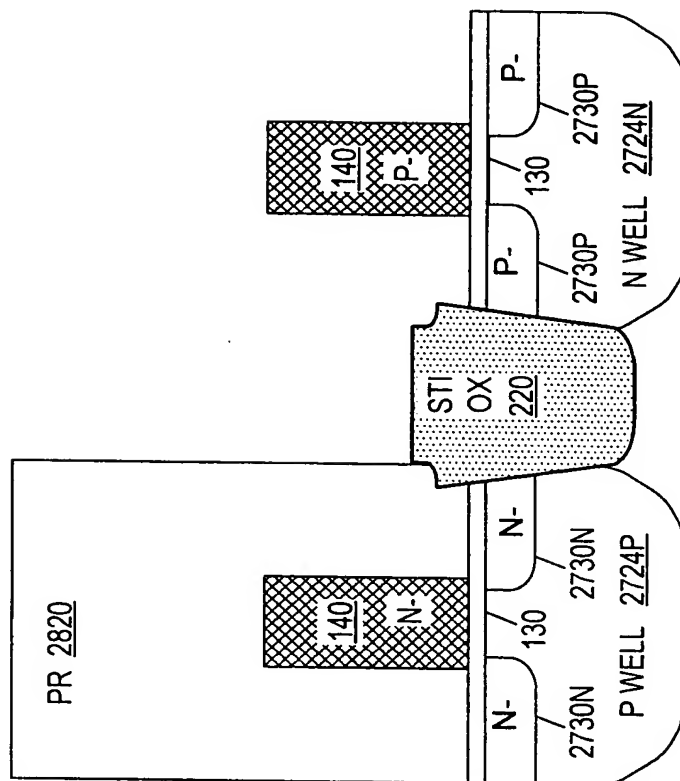


FIG. 22 P- 120

(PERIPHERY)

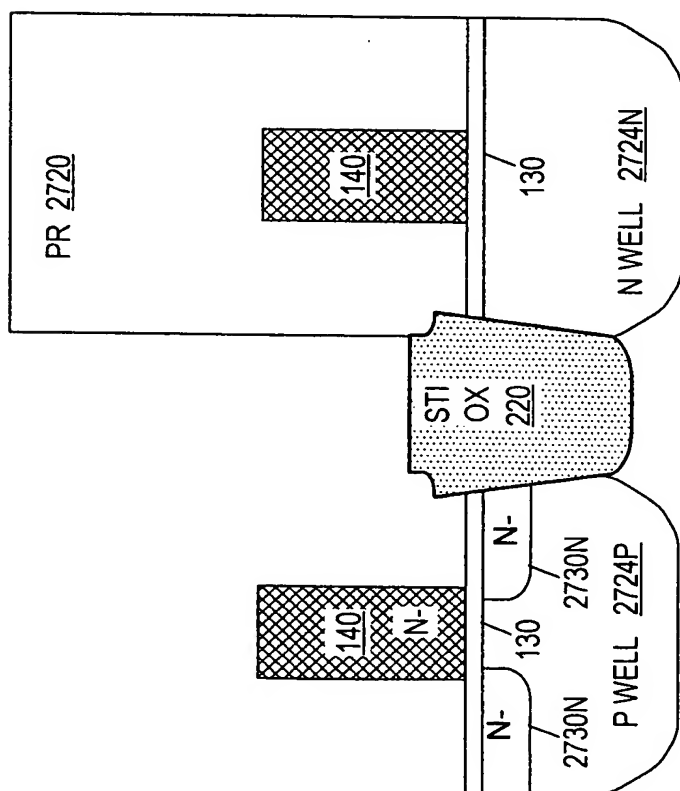
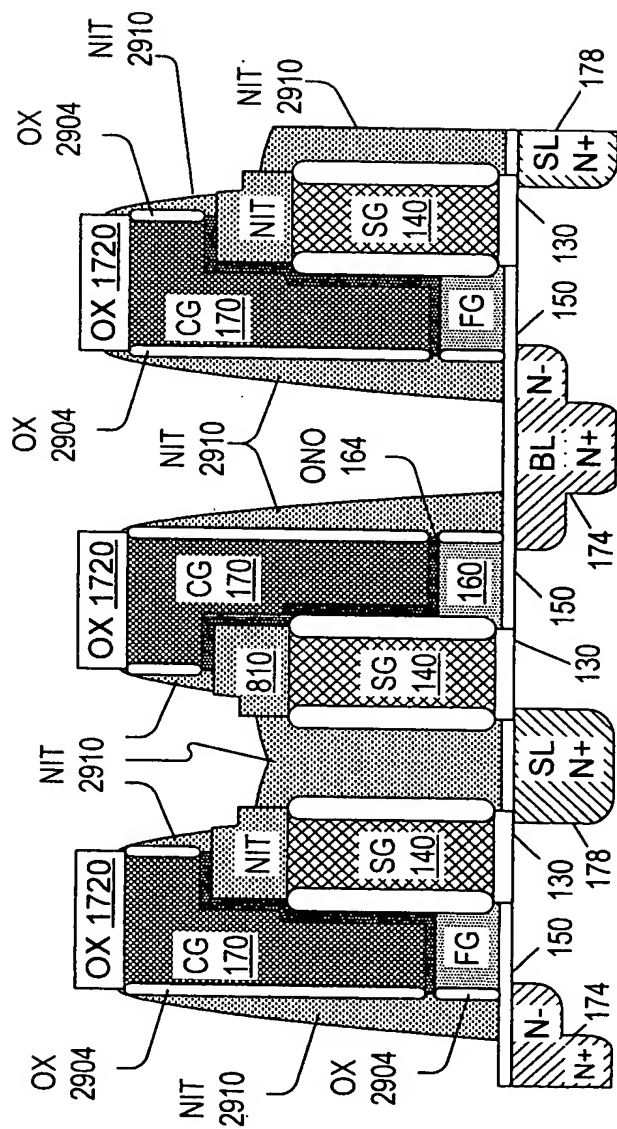
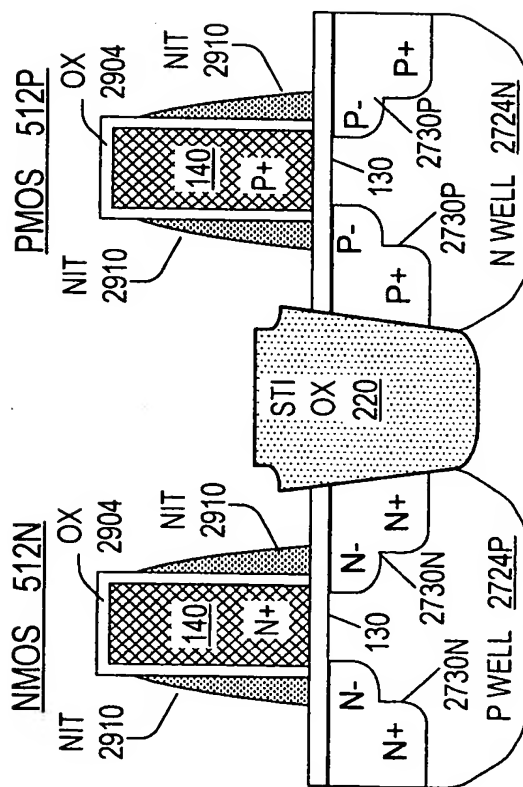


FIG. 21B P- 120

(PERIPHERY)

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FIG. 23A
(X1-X1')FIG. 23B
(PERIPHERY)

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FIG. 24A
(X1-X1')

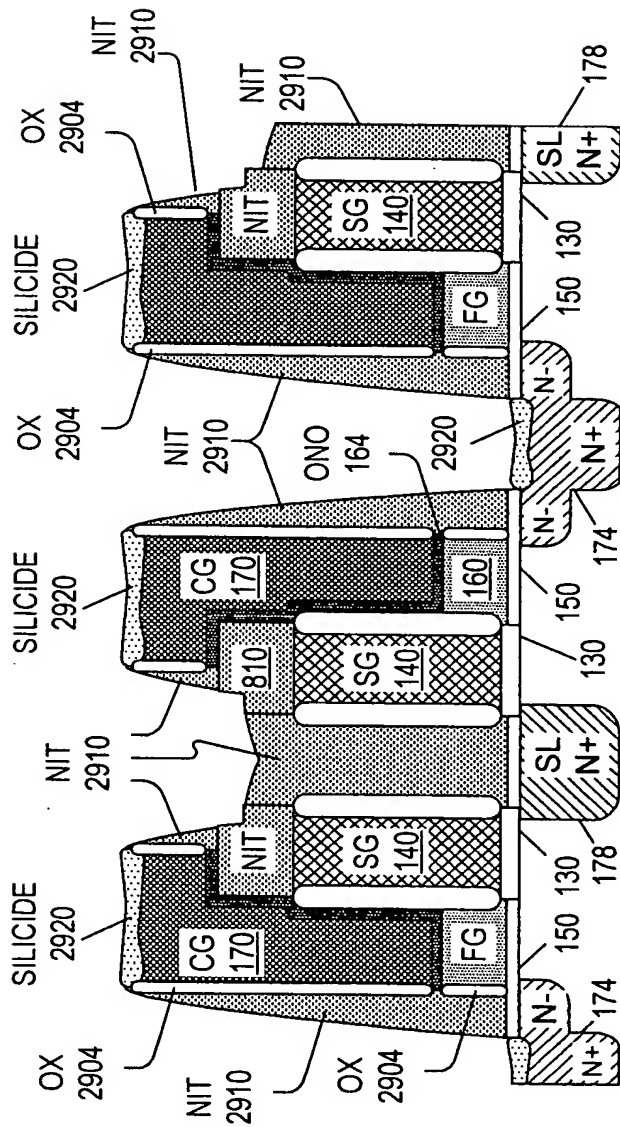
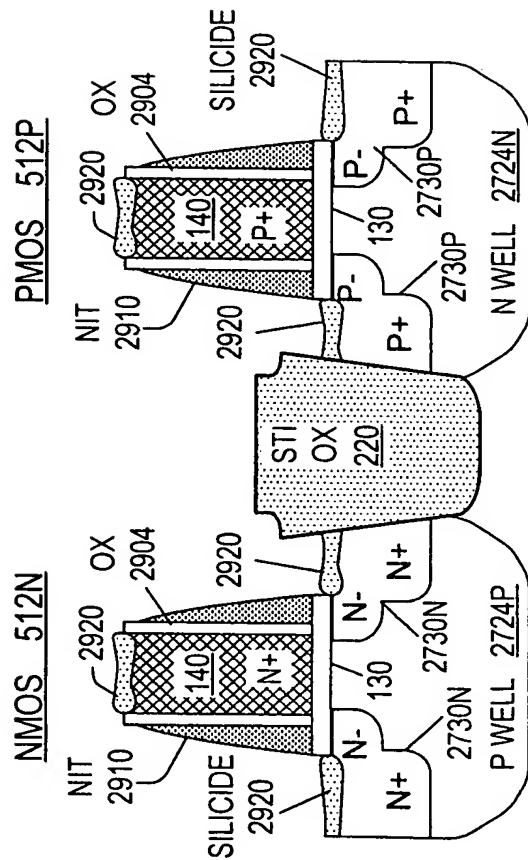


FIG. 24B
(PERIPHERY)



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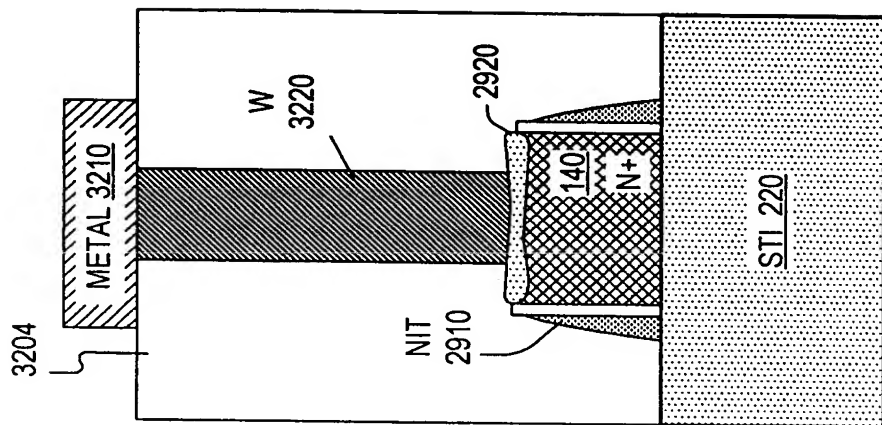


FIG. 25C
(PERIPHERY)

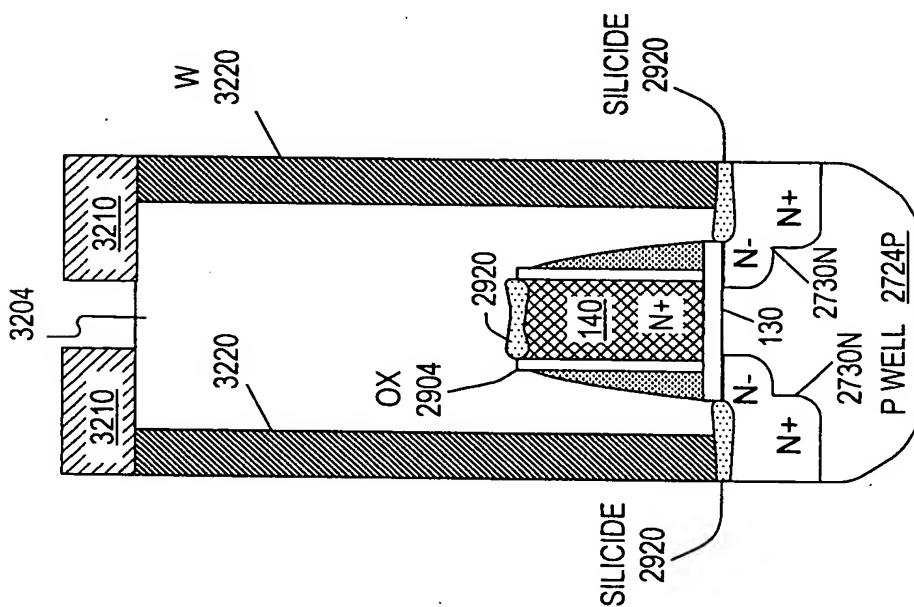


FIG. 25B
(PERIPHERY)

